

FIG.1

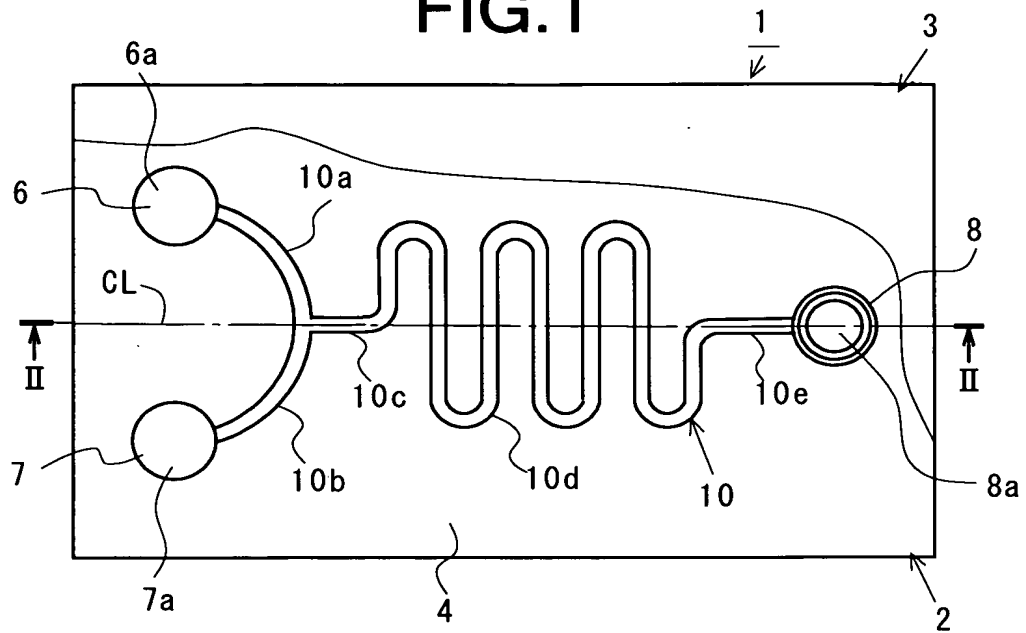


FIG.2

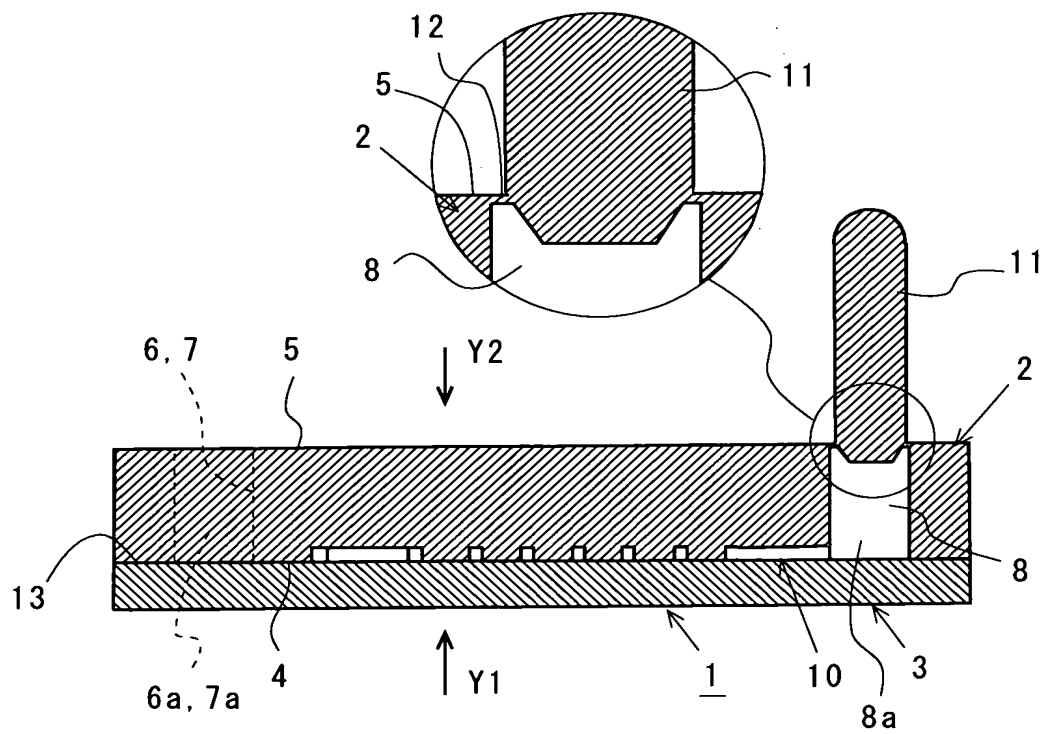


FIG.3

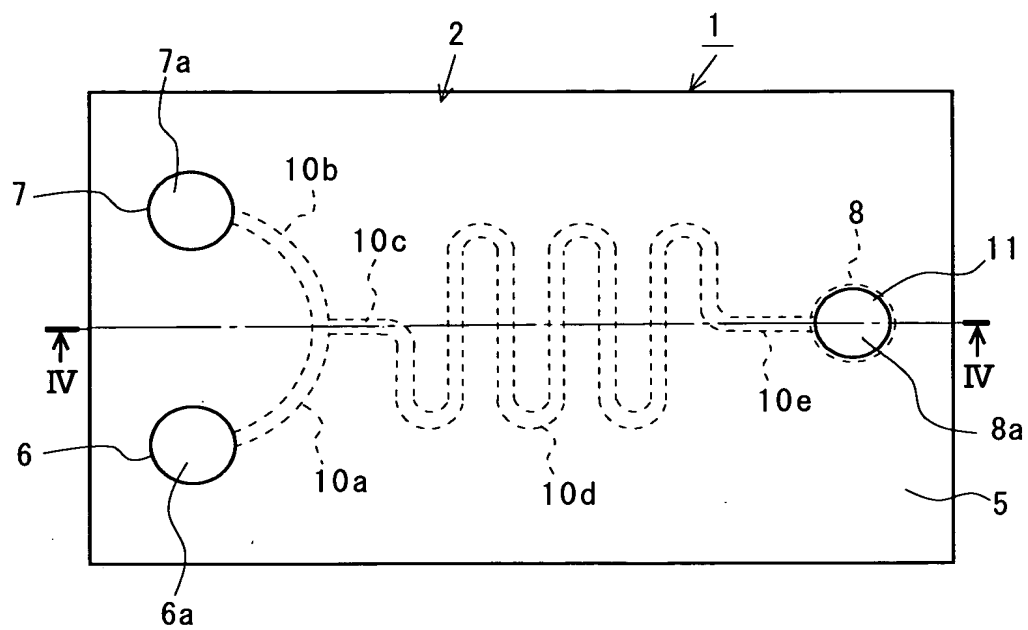


FIG.4A

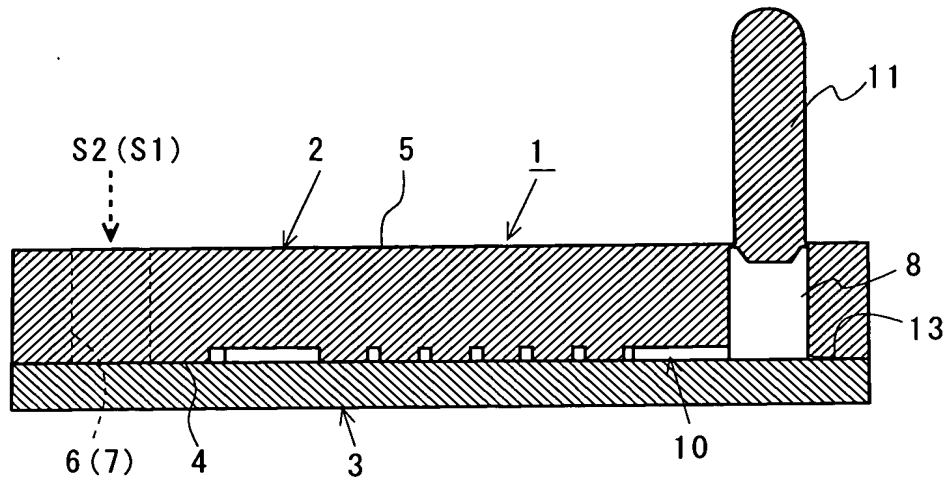


FIG.4B

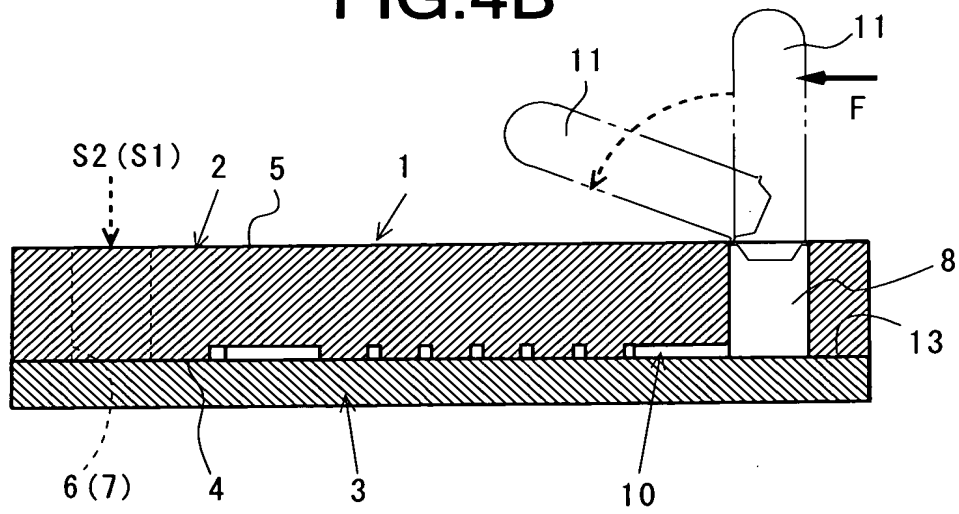


FIG.5A

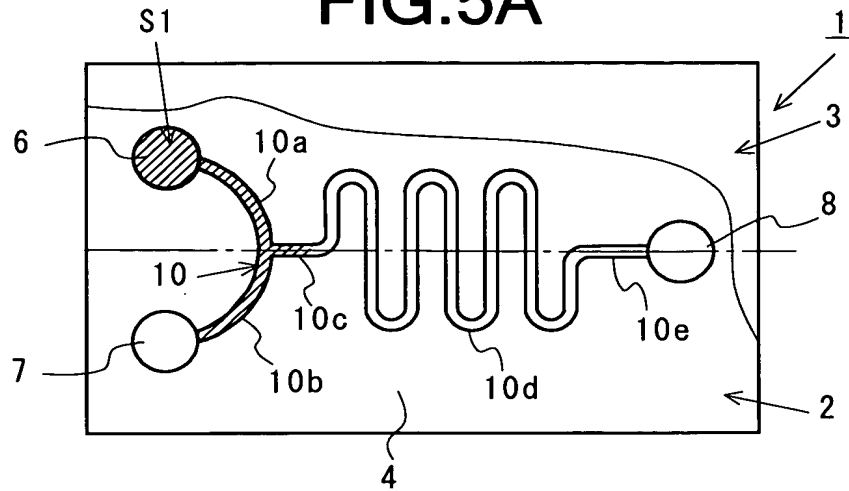


FIG.5B

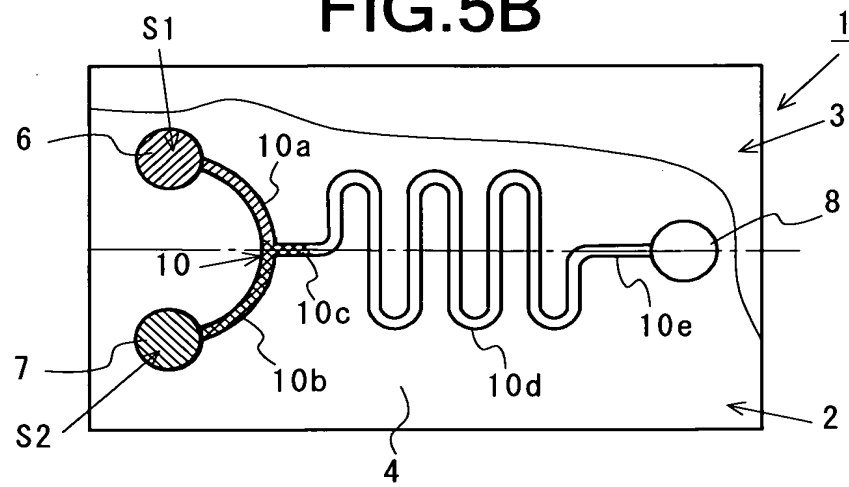


FIG.5C

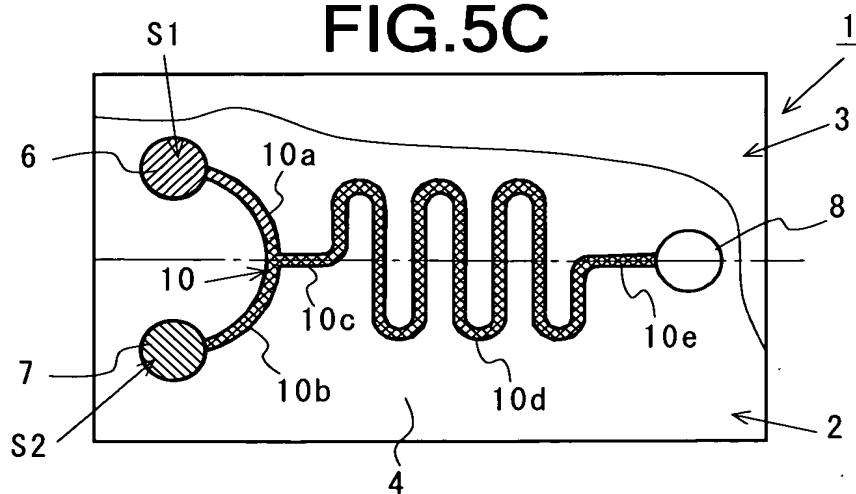


FIG.6

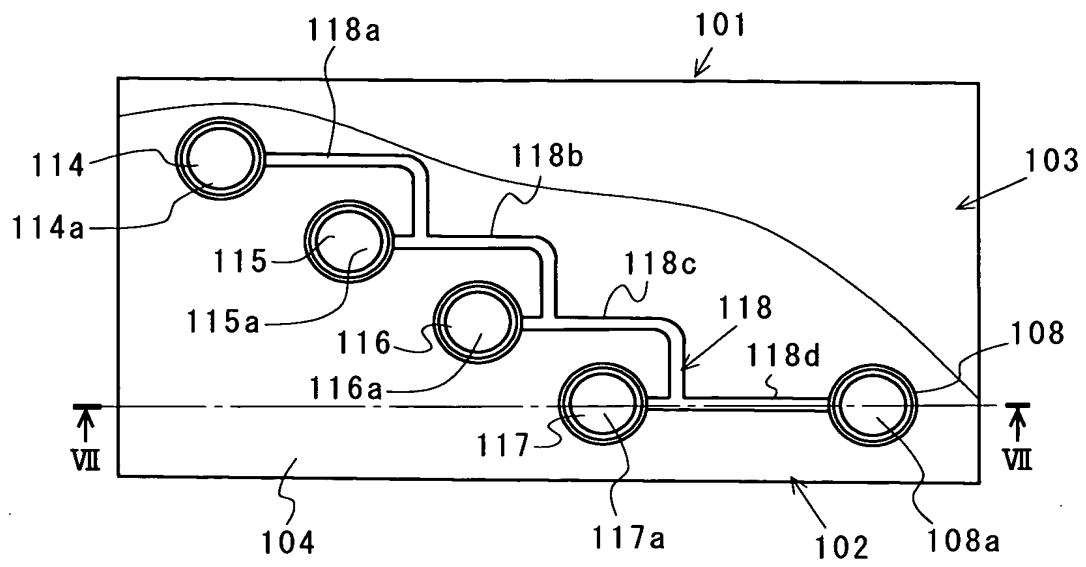
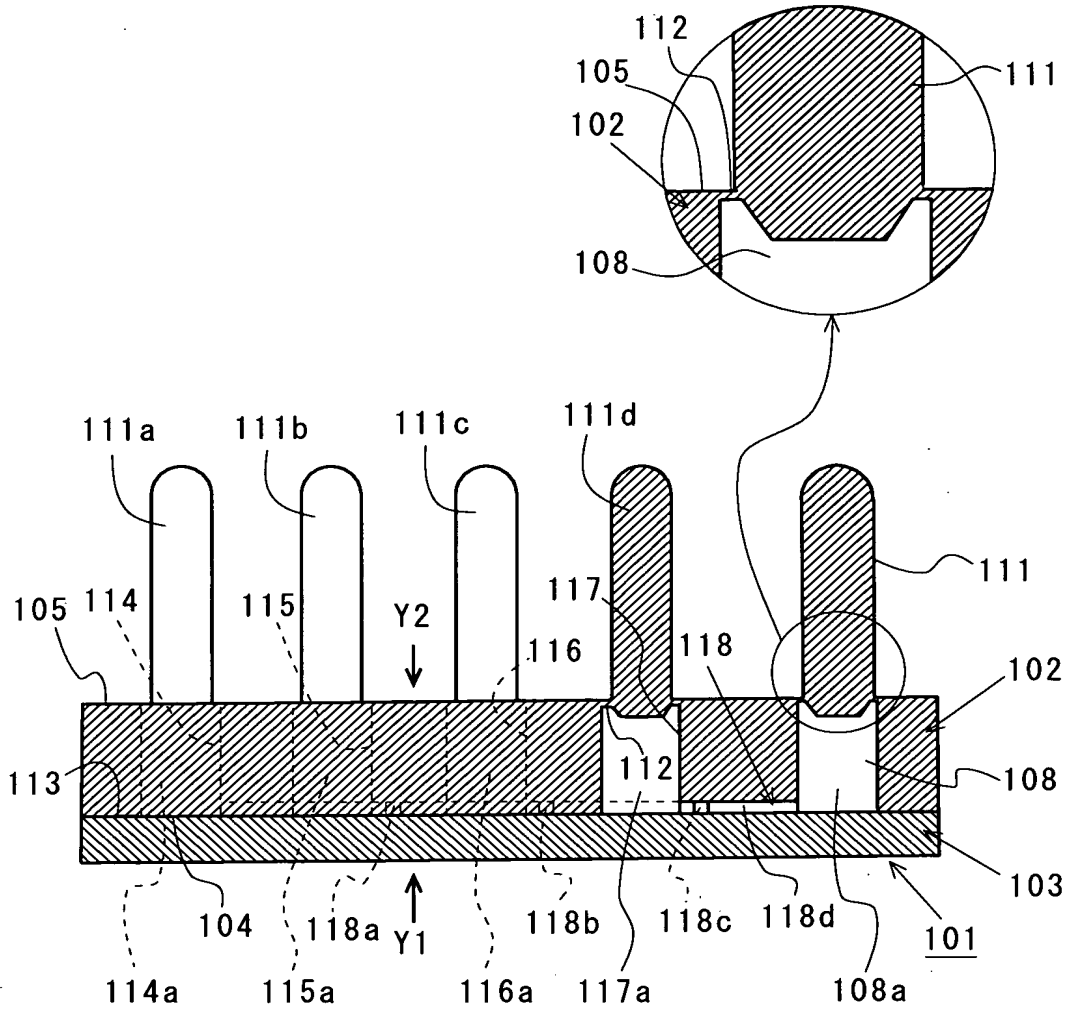


FIG.7



A schematic diagram of a rectangular device 101. The device contains four circular components labeled 111a, 111b, 111c, and 111d. These components are interconnected by dashed lines representing electrical or signal paths. Labels 112, 113, 114, 115, 116, 117, 118a, 118b, 118c, 118d, and 119 point to various connection points and segments along these paths. A horizontal dashed line with arrows at both ends is labeled IX. An arrow labeled 102 points to the right side of the device, and an arrow labeled 105 points to the bottom edge.

FIG. 9B

105, 101, 117, 111, 111a, F, 102, 108, 103, 104, 113, 118, 118a

FIG.10A

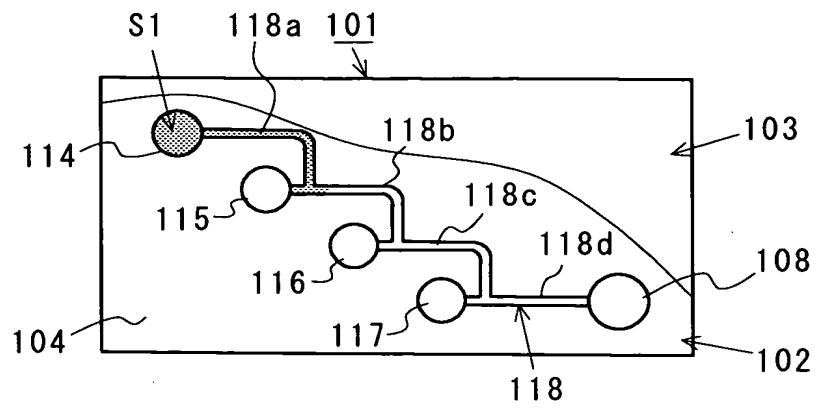


FIG.10B

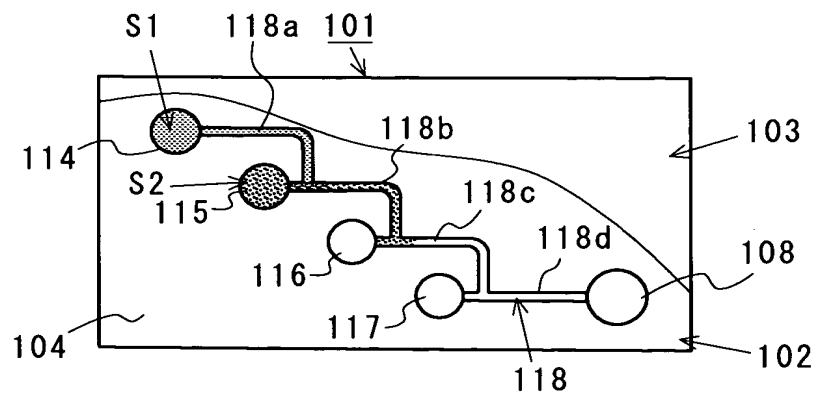


FIG.10C

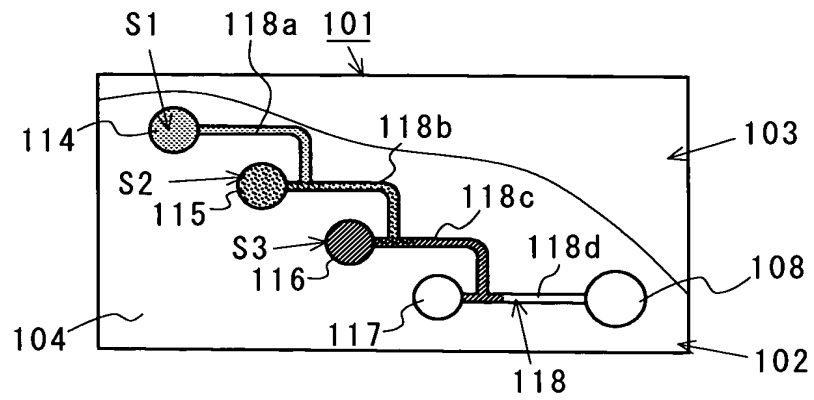


FIG.10D

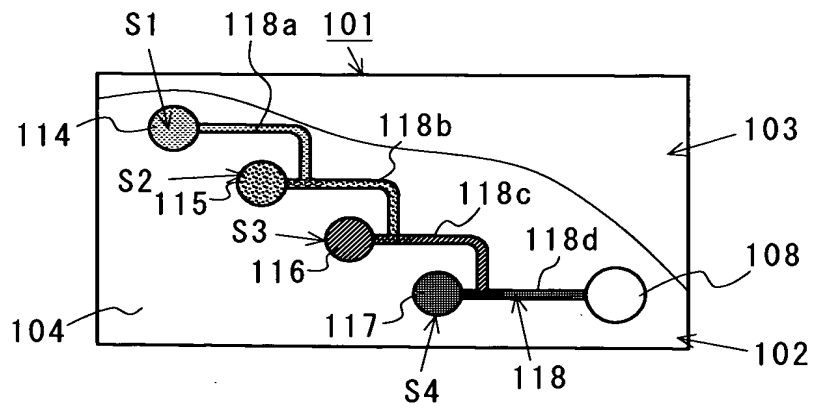


FIG.11

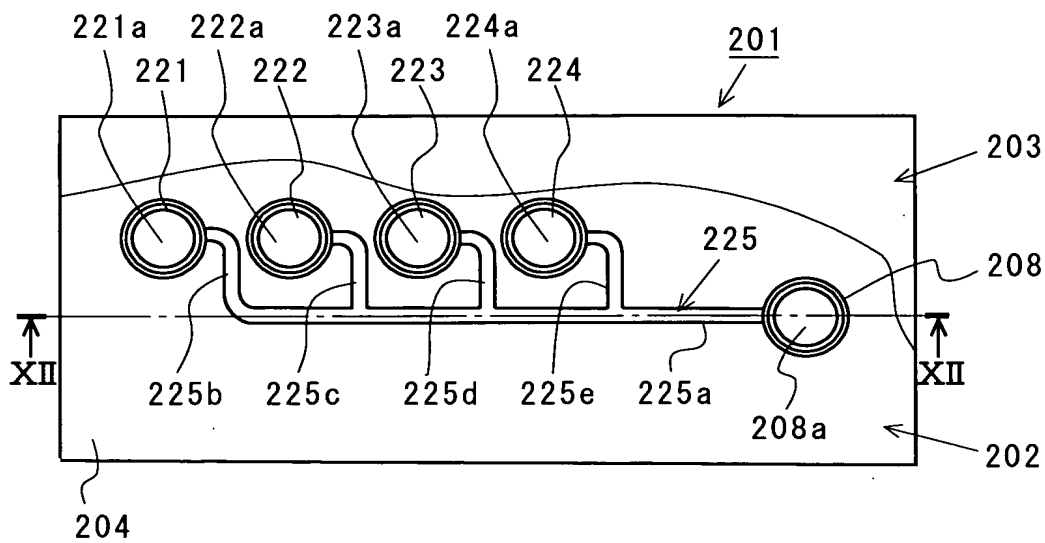


FIG.12

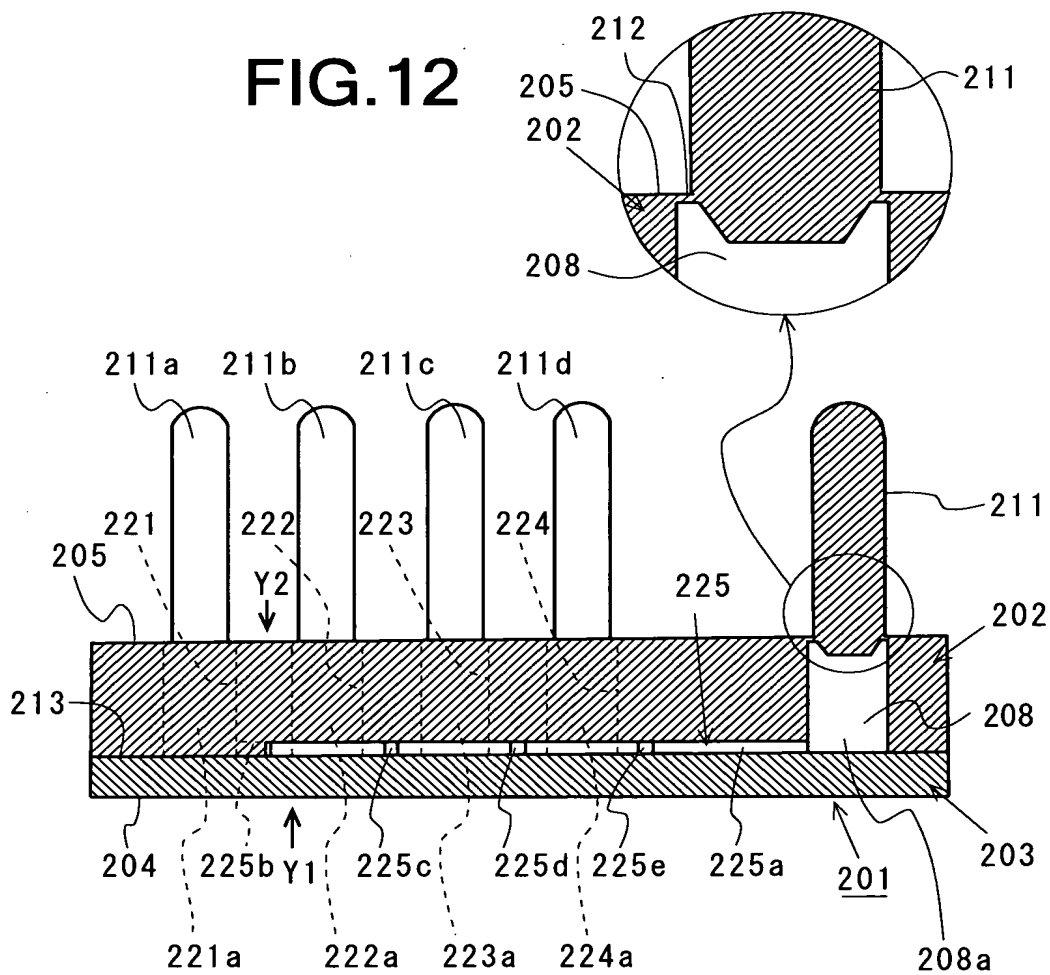


FIG.13

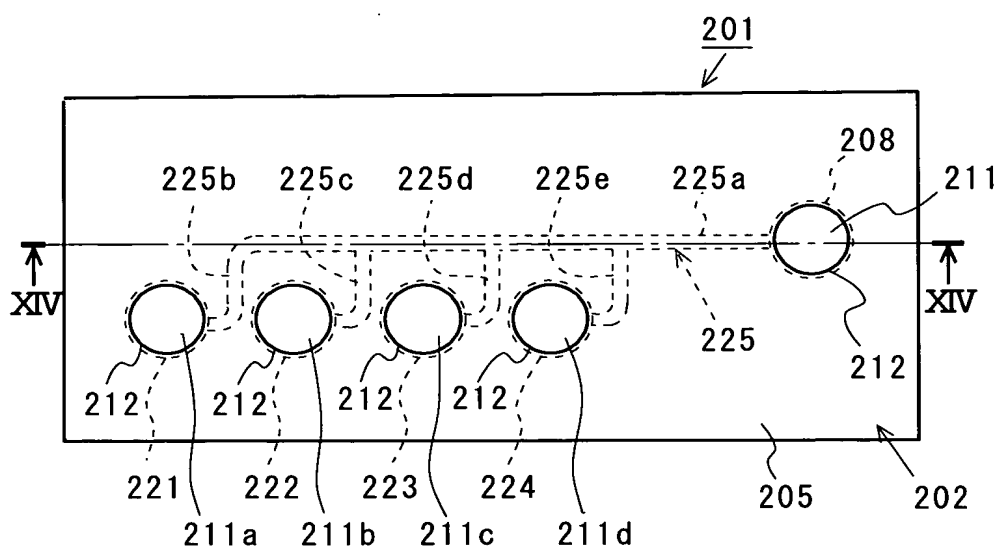


FIG.14A

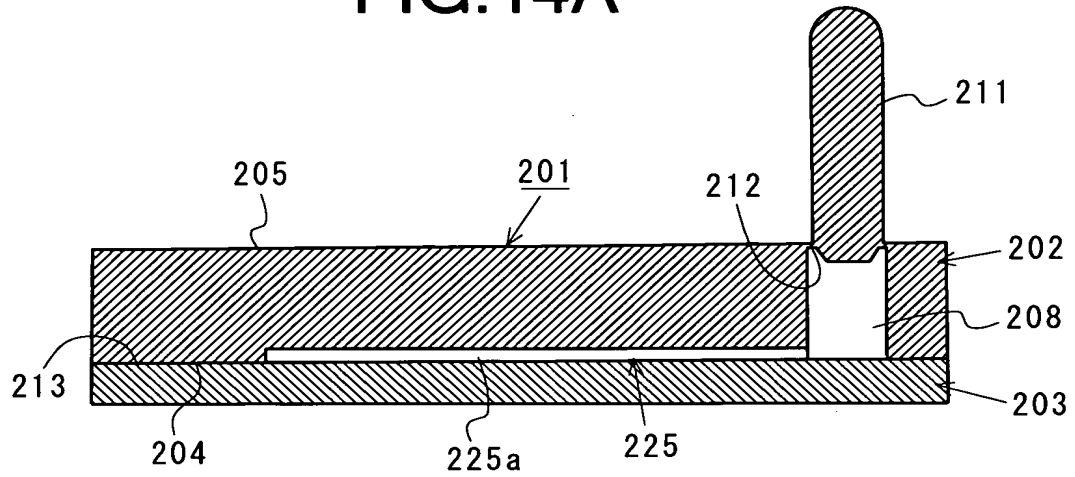


FIG.14B

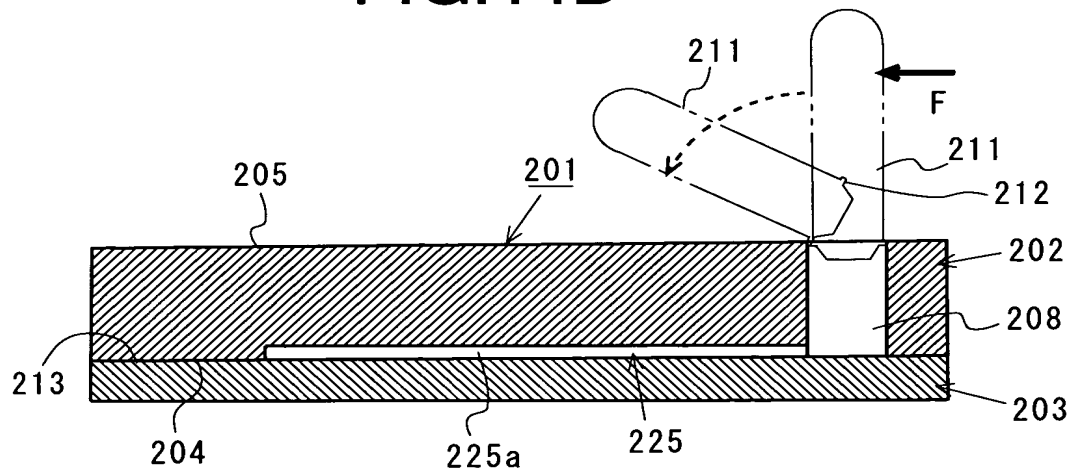


FIG.15A

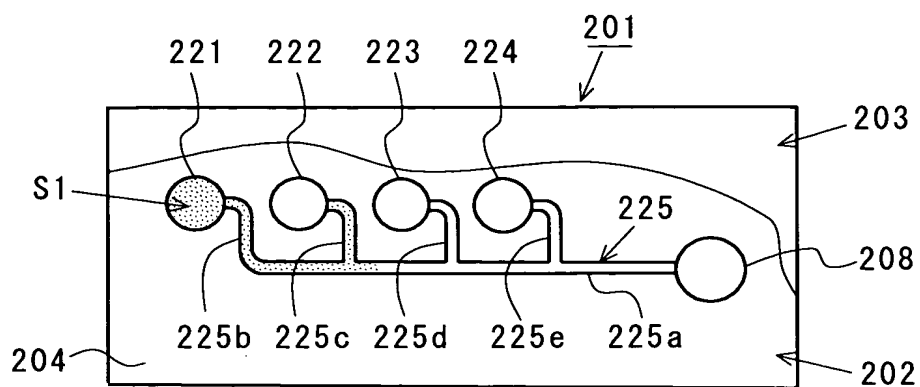


FIG.15B

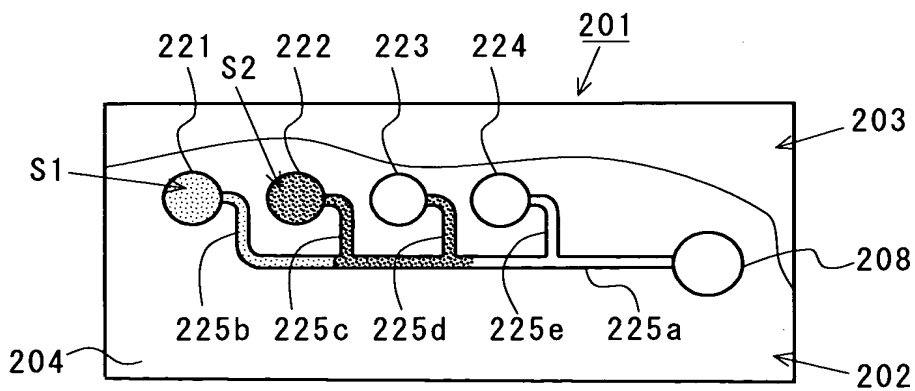


FIG.15C

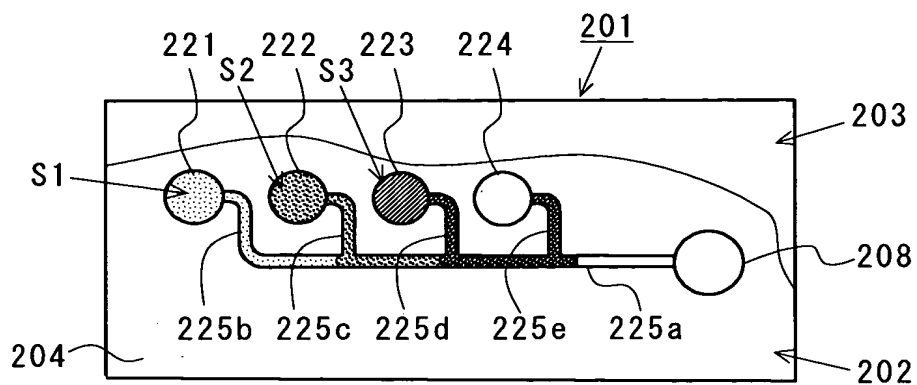


FIG.15D

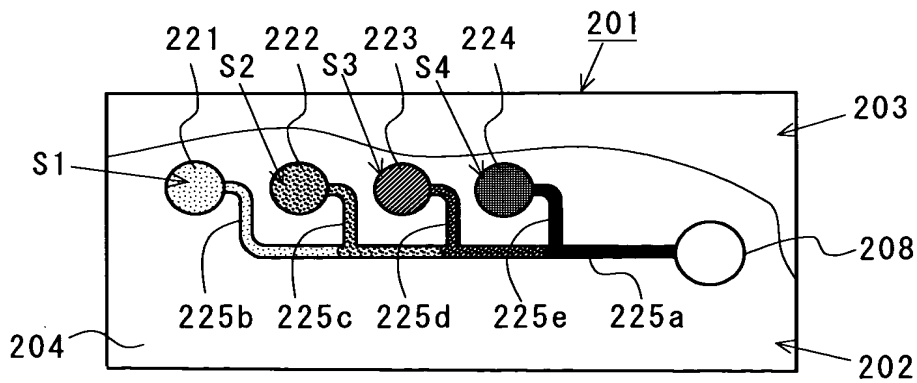


FIG.16

